

## Philosophy of Electronics

- 1.) A given element of an electronics assembly varies in qualities and properties for that of its particular placement and configuration in given circuits in terms of active and passive meaning; by its given implication to impart these to each given circuit element under relationships of inductive; capacitive; and resistive flows for that of the dual relation of a circuit to its elements.
- 2.) The properties of active and passive properties of inductance, capacitance, and resistance differ from that of the circuit elements under properties of each for that of implementation with that of either a given quality of transparency or corrugation of implication of whole circuit to element; under the terms of constructibility, transparency, configuration, and permanency.

Therefore; for example, the properties of a circuit element may be comprehended by its given interconnection with each other circuit element; under the provisions of a subsidiary relation to that of either a given circuit in completion or to that of the singular element under isolation.

When this is understood; a given circuit can be designed that is forward and backward compatible; and that of each given element designed around for that of a particular machine design furtherance; and that of specificity through compensation for that of any particular configuration of elements suited to any given specific task in whole and given completeness.

A part of a circuit is therefore only part of a given relation that possesses a dual capacity of derivation from principles to implementation; for which there is null redundancy for that of each given particular implementation; a subset of which is saturated for each fundamental property; when thought about in terms of topology; interconnectedness of graph, and mathematical expression of subsidiary local and global properties; emanating from each circuit element and conveyable to each in principle as under terms of operation through these two fundamental principles of design; of which are of number two in relation to the given difference found in one.

As a further principle; that of a given design cross referenced to circuit diagram indicating component placement and that of properties in implementation with that of a conjugate mathematical understanding and that of these two principles endow any circuit and number of elements with a novel interpretation of active and passive impedance and voltage to current relationships; for which the predictive operating point of a circuit is verifiable by commonly held understanding of basic elements of logical inference and deduction of design process and electronic principle; with a conjugate understanding of electromagnetic circuit properties in general; and which impart a given design with a unique meaning of its entire predictability.

These two principles of which have to do with the predictive capacity of electromagnetic design are the following for which each given relationship is predictive of that of a given electric folding:

- 1.) Voltage, current, and impedance follow as operators in the given passive space; are topologically projective; and of a subsidiary electric notion to that of the active; physical and electromagnetic notions of capacitance, inductance, and resistance of a given circuit in activity.
- 2.) Power and energy, information and reflectivity as one complete relation of conveyance.

## **Folding the Relation Part I:**

The relation of that of interior relation as formed; is potentiated by the realization that light in relation to the former of device physics with matter of charge like nature is formed from that of difference inclusive of relation of the three of impedance; inductance, resistance, and capacitance.

For instance; the relation of these in terms of topology is much more easily worked with when it is understood that the interior to exterior relation is indeed interior to exterior for that of a given vantage from the exterior to the method of operation reaching a lower topological layer; onto a former upper layer of which there is contactless separation between the operation laws of a given electromagnetic setup; a perfectly safe and effective way of operating on a device; in consistent relation to that of confirmability when it is understood that machine design operates in reverse for that of the given understanding of operation and construction as from inside to outside of the given relation in addition of steps followed into and out of the device in the process of formation.

This given means and implies through an imparted relation that of interoperation as a given compatible relation by the lucky return that makes the difference of two topologies only under similarity of formation for that of congruence of basic element of operation in dual terms; for which make the difference of inseparable similarity by conjugate relationships of unitary and cyclic relation through a balancing of each stage of congruence by that of following before a step taken in addition through the process of formation for former before former congruence of a latter stage as a three fold relation of either; in similitude to inductance, capacitance, and resistance, through the former relation of a flow; of fulfillment, capacity, and resilience of permanency of boundary of a given imaginary topological surface of admittance and reluctance.

This higher dimensional relation admits the flow of the current to voltage relationship to advance beyond the singular one dimensional relationship; and move off the graph through a tangential relationship for which there is preserved a unitary condition of evolution making a flow circumstantially independent of a degree of freedom and inhabiting of a higher dimensional manifold for which a degree of freedom is freed; and an other inclusively formed together; in admittance of a singular two dimensional surface of multidimensional capacity into the extruded relation of a higher dimensional relation of unlimited fulfillment, capacity, and resilience; with precision of increasing magnitude of and that of quality of self preserving character; inclusive phase alignment and self corrective features; making the difference of either flow and direction; for that of correlated exponents through which exact similitude of relationship is achievable.

The rules for progression are simple; for even that of component mismatch via differential where equivalent congruence makes a formative difference through a multiple of stages of linearity; and that of the progression geometrically is a multiplicative agent; through a similar logarithmic stage implementation for that of the conversion of differential equations from the relation of a given circuit diagram and into machine assembly and electromagnetic implementation; increasing precision beyond the conventional limitations of noise resilience and power frequency relation; for which the unlimited capacity of inversion of that of the characteristic forms a given solid relation of light field of higher dimensional geometry of a lower; self involutely expansive; at the cost of diminished character via nonlinear incursion of utility only to that of self recursion; an avoidable return when it is considered that a closed relation may be considered at the given end.

## Number Theory of Relation Opening Part I:

There are at least two natures for which computability of the return congruence of relationship is satisfiable for insurance of machine configuration ab initio before construction; one of which is binary and the other of which is as reliable is analog and each of which fit preformative factors of error margin to that of direct numerical applicability of each given subdivision of machine state:

1.) The first of which is a digital relation is a combination of representation through interface; for which the given equipartition of the machine is violated for operation and for which in addition computation is completed as that of reference to given comparative difference. For which an incongruent sequence of interfacing by digital operation by segmentation under equipartition reduces to a two fold relation for which the reproducibility of signal of random occurrence of machine relation (even with predictive reaction) produces a reliable return pattern of conversion to congruence. This implicates a return path for the information is deterministically non-chaotic; that which represents stability through that of a two fold division into quarters for which the machine design under irregularity is of use for that of for instance a three four and five fold relation by noticing the division of that of lowest common denominator. With this a conjugate representation under division eliminates that of it's given conjugate twin number of finite occurrence of components in series and parallel construction; leading to the direct conclusion that when a set of relations of inputs of variance equivalent to unity for the subdivisional aspect correlate with unique representations of information sequestering. The division is therefore as a given computable; for that of reception and transmission carry singular pieces of information that are also non-computable when traced to that of modular relation unless it is considered a given return is of division unto direct input to input process of binary nature as any either relation is as a given entire; an example of which is the digital conversion of a signal to direct high and low pass filters with un-reproducibility of information under absence of input reverse convolution.

2.) The second principle is one by where the same identified principle is applied from the digital domain under conversion unto an analog domain; despite analog to digital conversion for the end principle of that of rounding of bitwise information via duplicity of hardware machine state via reduction of gate pathway logarithmic conversion of equivalent error at replicated stages with that of each given zero and null relation of local variable measurement calibration of reference. This is that of which relates to a given inseparability or given separability of signal strength and form; when that of an analog signal is relatable to that of differential equation and modeling by that of pure form presence and that of corrupted signal presence with trigger or horizontal scaling and translation. When there is used a conventional signal analyzer; for what are interiorly that of two given gyrators back to back; with one ground reference free and one fixed for which then; either of two given signal transcriptions truncate as a result of gate threshold identification and loss of fidelity of a predictable nature. Identifying a midpoint of threshold intermediary state; between that of high; low; and passive relation into the given difference of one against the other; that of the relation of a midpoint machine state for which makes the difference of either above or below threshold of relation then indicates intermediary form; for when these relations surpass a given intersection; a low and high relation intermediary is a locable zero of free relation. This produces a direct locable union of digital interpretation; as that of which indicates decidably in similar fashion of curve intersection as phase and gain locking of either comparative signal by intermediary admittance of signal range of passthrough; for that of unlimited exclusive interior.

## **Folding the Relation Part II:**

When that of crystals are implemented; that of modes as a requirement of scrutability of given differences for through form reaching formless difference as multiplication in virtual relation; are decidable for that of segmentation of difference through articulation of machine interaction. For with selectivity that of either given division is reducible to singular pieces of information indicating zero and one relations of absence and presence. Where digital information defaults through the encompassing domain of light or sound cavitation that of specific interpretation of digital or analog information of presence or absence in violation of missing information. With this as a given default either a given component relation missing a leg of connectedness; or that of when a stage congruency is in violation; makes the relation of a full inclusion with exception of topology of connection the rule by which each given inaccuracy may be detected.

For then; as a consequence by bidirectional relation the given open interior relation of what of either transparent union of exterior surface to volumetric and hyper dimensional space of exclusion via inclusion is of dual exception. As an inclusion of information under convolution for which a given difference yields positive indication of hardware machine state operation and function; through which the verifiable stage connectedness of trace is complete and intact of a full relation of flow and conjugate process of balanced current and voltage relationships. For any given parallel or antiparallel relation of given balance or recurrence of graph to potential relation of voltage in congruency with any given gain to loss function there is an exceeding limitation.

As a consequence the voltage and current relationship at an outset is functionally self limiting to that of a fully potentiated flow by the encompassment of a preliminary reverse current flow which by a switch of closed topology relates to production of a potential swing that creates an impasse that reaches to threshold. Advancing forward a return current that passively sinks that of the active and open relation of connectedness by stage to stage congruency and via frequency limitation of upper relation a passive limit folds the relation of resistance into a completely passive relation without loss. Therefor the given capacitance and inductance cycle freely in a higher analogue space making time the circumstantial factor by which a given open and free flow possesses independence of machine state without ceasing as an unlimitedly passive current which cycles and voltage matches for that of impedance and reflectivity as an invisible open interior.

Therefore frequency is unlimited in the lower direct current state with isomorphism making the flow under completion of a tidal expression for which direct current also isomorphically expresses congruence in completion to either a given direct open flow of interior to exterior. For which the complete and open relation freely moves in either a rectilinear and digital fashion of free differential to finite space element of any geometry or analog and differential curvilinear stereo isomorphic or meromorphic differential manifold space of smooth union. Then, either given subcomponent is confirmable through reduced formation of light field congruence; making a formative difference of choice before action of effective return via singular scale free difference. When each stage and operation of part in effect of universal nature influences machine state and direct input output conjunctive differential of either or simply disconnected nature; a result and consequence of light field singular exceptionable exclusive partition of subconstituent element stylus; results in the exception to congruence which is a formed reaction event of nature, state and delimitation of exclusive return as deterministic inclusion of freely independent choice function.

## **Proof of Independence and Completion of Machine Design Part I:**

This is but one possibility of machine design for which a meromorphic smooth manifold of relation of higher hyperdimensionality is realizable; as there are an infinite number of effective configurations. However the given difference of subconstituent elements are finitely arranged into an effective difference of topological configuration of set subset relation of seven fold tier difference of circle from point like exception; and therefore constitute a torus. For that of a filled four dimensional two dimensional space; and that of one dimensional extension from point to point like relation upon and along that which is a fully articulated smooth extension of line and line folded into a singular point to surface like relation of difference of volume from space of surface; a two dimensional intersection of filling and of volumetric three dimensional space exists between each one double cover of three ball and four dimensional surface difference of a three dimensional boundary. By line like surface extension with point to curve like difference with surface like extension to three fold relation of line to line to line with point to surface to volume like relation and then to four dimensional extremity of finite point like relation of boundary like surface; limitation of the device to a series of points, lines, surfaces, and volumes, of each a hyper manifold nature of covering and disconnected modular relation up to infinite expressible order is encoded in one two dimensional four pointed rectangle; that of the relation of a binary piece of information to an infinite dimensional four fold relation of dimension four as given four volume.

However there may be an infinite number of configurations; this is the only one admissible generalized hardware configuration for which the cycle of machine states and for which topology is an open four dimensional volume of layered structure as a hypercotrochoid embedded space with equivalent relation to any physical object in space and time. For independent representation of holographic nature with pure independence of finite machine state under comparability; any simpler design would violate the rule of closure before opening of a state. Any more complex relation would be overly orchestrated even if of conversion from digital to analog set so as not affording that of implementation via what reduces effectively to independence of end machine state. With that of either given parallel and antiparallel return; with the exception of machine state via microchips and excess components of which are unrequired; these merely hone the configuration but do not alter the given device machine state; and in this capacity it has the greatest machine state volumetric data capacity per count of minimal device components.

Any device of this characteristic as constructed therefore possesses a maximal within minimal structure of which is determinable a priori by construction methods as defect free; for when each given layer of design through the process of creation limits end occurrence of unnecessary extra equivalent components. Therefore from that of stage to stage configuration for which there is but one; to that of end and former limitation for which there is one; and that of given balancing of which is potentiated but unnecessary for the corrective attribute of finite interconnect topology; each therefore makes the machine at the balancing point of self repairability and hardware virtualization. This edge of design is then a finite design exclusively for the reason that the exception of one degree less or more is in excess; and therefore such a machine is situated at the exact point of machine design which effectively disambiguates this from virtualization at a lowest entropy point and maximal enthalpy point with balance of extension equivalent asymptotically to one; for that of unity in representation of any such given analog point is fixed to it's end state; recurrent in time cyclically; and spatially of union one; for that of inseparability of occurrence.